

CLAIMS

Having thus described the invention, what is claimed as new and desirable to be secured by Letters Patent is as follows:

1 1. A method for separating a semiconductor device from substrate in a fixture having
2 a shearing element where the semiconductor device is attached to the substrate by solder
3 connections to form an assembly, the method comprising:
4 applying a loading force to drive the shearing element away from its home position to
5 which it is urged by a mechanical biasing force into a loading position,
6 loading the assembly of the substrate and the semiconductor device into the fixture with
7 the shearing element proximate to the semiconductor device,
8 removing the loading force to apply a shearing force derived from the mechanical
9 biasing force which is and applied by the shearing element to the semiconductor device, and
10 heating the solder connections of the assembly in the fixture to a predetermined
11 temperature.

1 2. The method of claim 1 with the fixture including a top member with a window
2 therethrough for the semiconductor device and the shearing element located below the top
3 member.

1 3. The method of claim 1 wherein the shearing element comprises a slidable blade.

1 4. The method of claim 1 wherein the shearing element is a blade affixed to a slidable
2 element that is connected by a linkage to a coil spring which applies the mechanical biasing
3 force thereto.

4 5. The method of claim 1 wherein the predetermined temperature is below the melting
5 temperature of the solder at which shearing of the solder connections occurs.

1 6. The method of claim 1 wherein:

2 the fixture includes a top member with a window therethrough for the semiconductor
3 device and the shearing element located below the top member,

4 the shearing element comprises a slidable blade,

5 the slidable blade is affixed to a slidable element that is connected by a linkage to a coil
6 spring which applies the mechanical biasing force thereto,

7 when the loading force is removed the slidable blade moves from proximate to the chip
8 into contact with the chip thereby applying a shearing force to the chip in response to the
9 biasing force from the coil spring, and

10 the predetermined temperature at which shearing of the solder connections occurs in
11 response to the shearing force is below the melting temperature of the solder.

1 7. A method for removing a circuit chip from a substrate in a fixture having a shearing
2 element where the chip is secured to a substrate by bonding elements, comprising:
3 applying a loading force to move the shearing element from a mechanically biased home
4 position into a loading position against a biasing force,
5 then loading the substrate with the chip onto the fixture,
6 removing the loading force to arm the shearing element into a position in contact with
7 the chip, so the biasing force is applied by the shearing element to the semiconductor device,
8 and
9 heating the assembly located in the fixture to a predetermined temperature until shearing
10 of the bonding elements occurs while continuously applying the mechanical force with the
shearing element.

1 8. The method of claim 7 wherein the shearing element comprises a slidable blade.

1 9. The method of claim 7 wherein the shearing element is a blade affixed to a slidable
2 element that is connected by a linkage to a coil spring that applies the biasing force thereto.

1 10. The method of claim 7 wherein the shearing element is a blade affixed to a slidable
2 block that is biased to a home position by a coil spring which applies the biasing force
3 thereto.

1 11. The method of claim 7 wherein:

2 the shearing element is a blade affixed to a slidable block which is connected by a
3 screw that is biased to a home position by a coil spring which applies the biasing force
4 thereto, and

5 an adjustable element is provided for adjusting the bias applied by the coil spring.

1 12. The method of claim 7 wherein the bonding elements comprise solder and the step is
2 performed of heating the solder to a temperature facilitating shear and removal of the chips
3 from the substrate while the solder is in a solid state.

1 13. The method of claim 7 wherein the substrate is loaded into a seat in an upper element
2 with the chip extending through a window therein.

1 14. The method of claim 7 wherein:

2 the substrate is loaded into a seat in an upper element of the fixture with the chip
3 extending through a window therein,

4 the shearing element is connected to a slide element that is biased to a home position by
5 a coil spring which applies the biasing force thereto, and

6 an adjustable element is provided for adjusting the bias applied by the coil spring.

1 15. The method of claim 14 wherein the slide element includes a nest located below the
2 window adapted and located for catching a chip sheared from a substrate.

1 16. Apparatus for separating a semiconductor device from a substrate where the
2 semiconductor device is attached to the substrate by solder connections to form an assembly,
3 comprising:

4 a biasing element for applying a loading force to drive a shearing element away from its
5 home position to which into a loading position under a fixture it is urged by a mechanical
6 biasing element,

7 a loading element for placing the assembly of the substrate and the semiconductor
8 device into a fixture with a window therethrough for the semiconductor device with the
9 shearing element in contact with the semiconductor device and armed for shearing the
10 semiconductor device from the substrate.

1 17. The apparatus of claim 19 wherein the shearing element comprises a slidable blade.

1 18. The apparatus of claim 19 wherein the shearing element is a blade affixed to a slidable
2 element that is connected by a linkage to a coil spring which applies the biasing force thereto.

1 19. Apparatus for removing a circuit chip from a substrate where the chip is secured to a
2 substrate by bonding elements, comprising:

3 a biasing element for applying a loading force to move a shearing element from a
4 mechanically biased home position into a loading position under a fixture against a biasing
5 force, *contd*

6 a loading element for placing the substrate with the chip onto the fixture, and

7 a retaining element for holding the shearing element in a position in contact with the
8 chip, so the biasing force is applied by the shearing element to the semiconductor device.

1 20. The apparatus of claim 19 wherein the shearing element comprises a slidable blade.

1 21. The apparatus of claim 19 wherein the shearing element is a blade affixed to a slidable
2 element that is connected by a linkage to a coil spring that applies the biasing force thereto.

1 22. The apparatus of claim 19 wherein the shearing element is a blade affixed to a slidable
2 block which is connected by a screw that is biased to a home position by a coil spring which
3 applies the biasing force thereto.

1 23. The apparatus of claim 19 wherein:

2 the shearing element is a blade affixed to a slidable block that is connected by a screw
3 that is biased to a home position by a coil spring which applies the biasing force thereto, and
4 an adjustable element is provided for adjusting the bias applied by the coil spring.

1 24. The apparatus of claim 19 wherein the bonding elements comprise solder adapted to be
2 heated to a temperature facilitating shear and removal of the chips from the substrate while
3 the solder is in a solid state.

4 25. The apparatus of claim 19 wherein the substrate is loaded into a seat in an upper
5 element with the chip extending through a window therein.

6 26. The apparatus of claim 19 wherein:

the substrate is loaded into a seat in an upper element with the chip extending through a
window therein,

the shearing element is connected to a slide element which is connected by a screw that
is biased to a home position by a coil spring which applies the biasing force thereto, and
an adjustable element is provided for adjusting the bias applied by the coil spring.

1 27. The apparatus of claim 24 wherein the slide element includes a nest located below the
2 window adapted and located in a position for catching a chip sheared from a substrate.

Add A4 }